953	منـــــ	Subclass	CLASSIFICATION
659/494		Class	ISSUE CLA

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PATENT NUMBER

U.S. บักิ LITY Pate		
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CL	324	SUBCLASS	نَ	ART UNIT	758	EXAMINER Land
٠	APPLICATION NO. 09/494953	, 20	CLASS 324	SUBCLASS	ART UNIT 2858	EXAMINER 1012 12cm
	Yoshinar Yoshinar	u Kato			213	3

Test method and test circuit for electronic device

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ISSUING CLASSIFICATION						
ORIGIN	AL CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)			
INTERNATIONA	L CLASSIFICATION					
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TERMINAL		DRAWINGS		CLAIMS ALLOWED		
DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
The term of this patent subsequent to (date)				NOTICE OF ALI	OWANCE MAILED	
has been disclaimed.	(Assistant Examiner) (Date)					
The term of this patent shall not extend beyond the expiration date						
of U.S Patent. No.				ISSUE FEE		
				Amount Due	Date Paid	
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The terminalmonths of this patent have been disclaimed.				ISSUE BATCH NUMBER		
uns patern nave been discialined.	(Legal Instrume	nts Examineri	(Date)			
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